

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re New PATENT Application of )  
Shunpei YAMAZAKI et al. )  
Serial No. Not Yet Assigned ) Attn: Applications  
Filed: February 11, 2000 ) Branch  
For: SEMICONDUCTOR DEIVCE )  
AND METHOD OF )  
MANUFACTURING THEREFOR ) Date: February 11, 2000

INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

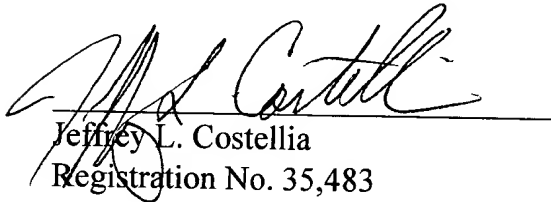
In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

WO Patent 90/13148 and U.S. Patents 5,247,190 and 5,399,502 are listed

in the patent family of Japanese Patent Laid-Open No. 10-092576.

Respectfully submitted,



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